Search Notes

Application No.	Applicant(s)
09/852,683	ADACHI, KAORU
Examiner	**Art Unit**	
Christopher E. Lee	2112	

SEARCHED

Class	Subclass	Date	Examiner
710	316 312 311 307 305 304 107 58	10/23/2003	CEL
710	29	10/23/2003	CEL
713	600 500	10/23/2003	CEL
713	400	10/23/2003	CEL
370	444	10/23/2003	CEL
331	49	3/11/2004	CEL
327	291	3/11/2004	CEL
710	306 309	9/29/2004	CEL
710	316	9/28/2004	CEL
710	316 305	3/26/2005	CEL
713	501	3/26/2005	CEL

SEARCH NOTES (INCLUDING SEARCH STRATEGY)

	DATE	EXMR
EAST Search - Bus switching, Multi-speed bus operation, multiple clock domains processing	10/23/2003	CEL
EAST Search - Switching clock signal for variable range of clock speeds	3/11/2004	CEL
EAST Search - Bus partitioning method and apparatus	9/29/2004	CEL
east Search - Partitioned Bus operation with dynamic speed adjustment according to bus switching oiperation	3/26/2005	CEL

INTERFERENCE SEARCHED

Class	Subclass	Date	Examiner